

ABSTRACT OF THE DISCLOSURE

A signal switching apparatus is provided which is capable of performing checking in a manner that a plurality of output channels can be discriminated in checking the assignment of the plurality of output channels to buses to which signals are input. A plurality of signals input to a mix bus comprised of six buses are selectively output to two analog outputs in accordance with assignment performed by an output assigning device. First checking signals for three channels among six channels from a first oscillator are directly input to an adjuster, and first checking signals for the rest of the six channels from the first oscillator are input to a signal switching device. Second checking signals for all of three channels from a second oscillator are input to the checking signal switching device. The checking signal switching device selectively outputs the input first or second checking signals to the adjuster. The adjuster selectively outputs the input first and second checking signals to desired buses and at desired volumes in accordance with settings of the adjuster, and the first and second checking signals are output via respective analog outputs in accordance with settings of the output assigning device.